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Call for Papers

The IEEE International Symposium on Design and Diagnostics of Electronic Circuits and Systems provides a forum for exchanging ideas, discussing research results, and presenting practical applications in the areas of design, test, and diagnosis of electronic circuits and systems. The DDECS Symposium series has been organized by these European countries: Czech Republic (1997, 2002, 2006, 2009, 2013), Poland (1998, 2003, 2007, 2014), Slovakia (2000, 2004, 2008), Hungary (2001, 2005), Austria (2010), Germany (2011) and Estonia (2012).

DDECS 2015 will take place in Belgrade, the Serbia's capital. Belgrade sits at the confluence of the two rivers (Danube and Sava), both of which draw you like a magnet when the sun is out and the temperatures rise. Belgrade is the home town of the astrophysicist Milutin Milanković, mathematician Mihailo Petrović, and winner of the Nobel Prize in literature Ivo Andrić. It hosts around hundred thousand students at more than 10 universities. Over eight hundred thousand tourists are attracted every year by the Belgrade's cultural events (International Film Festival, Belgrade International Theatre Festival, and Belgrade Jazz Festival), museums (National Museum and Nikola Tesla Museum), exhibitions and nightlife.

Topics of Interest

SoC and NoC Design and Test
Design and Test in Nano-Technologies
ASIC/FPGA Design
Analog, Mixed-Signal, RF Design and Test
Built-in Self-Test and Self-Repair
ATE Hardware and Software
Bio-Inspired Hardware
Design for Testability and Diagnosis
Design Verification/Validation
On-line Testing

Formal Methods in System Design
Embedded Systems Testing
Hardware/Software Co-Design
Memory, Processor Testing
IP-based Design
MEMS Testing
Logic Synthesis
Physical Design
Defect/Fault Tolerance and Reliability
Dependable HW/SW Systems

Submissions

Prospective authors are cordially invited to submit original papers using the electronic submission system at the Symposium web page. Papers in English with a length of 6 pages maximum in IEEE conference style are expected. Specialized student and industrial sessions, as well as embedded tutorials, will be organized at the symposium. Accepted papers will be included in the Symposium Proceedings and submitted for inclusion into IEEE Xplore as well as other Abstracting and Indexing (A&I) databases.

Important Dates

Paper submission deadline
Notification of acceptance
Publication-ready final version due
Symposium

extended by 25. January 2015

13. March 2015
20. March 2015
22. - 24. April 2015

Further Information

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